jc872 U.S. PTO
09/995085
11/27/01

| Class | Subclass | ISSUE CLASSIFICATIO |
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PATENT NUMBER

# U.S. **UTILITY** Patent Application

| O.I.P.E. | PATENT DATE |
|---|---|
| SCANNED Q.A. | |

| APPLICATION NO. | CONT/PRIOR | CLASS | SUBCLASS | ART UNIT | EXAMINER |
|---|---|---|---|---|---|
| 09/995085 | F | ~~364~~ 174 | 255 | ~~2835~~ 2827 | DINH |

APPLICANTS

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TITLE

Multilayer circuit board and method of manufacturing the same

PTO-2040
12/99

## ISSUING CLASSIFICATION

| ORIGINAL | | CROSS REFERENCE(S) | |
|---|---|---|---|
| CLASS | SUBCLASS | CLASS | SUBCLASS (ONE SUBCLASS PER BLOCK) |
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| INTERNATIONAL CLASSIFICATION | | | |
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| ☐ TERMINAL DISCLAIMER | DRAWINGS | | | CLAIMS ALLOWED | |
|---|---|---|---|---|---|
| | Sheets Drwg. | Figs. Drwg. | Print Fig. | Total Claims | Print Claim for O.G. |
| | | | 4C | | |
| ☐ The term of this patent subsequent to _____ (date) has been disclaimed. | (Assistant Examiner) (Date) | | | NOTICE OF ALLOWANCE MAILED | |
| ☐ The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ | (Primary Examiner) (Date) | | | ISSUE FEE | |
| | | | | Amount Due | Date Paid |
| ☐ The terminal ____ months of this patent have been disclaimed. | (Legal Instruments Examiner) (Date) | | | ISSUE BATCH NUMBER | |

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